

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of

Conf. No: 3750

Sweyyan SHEI et al

Patent No: 7,120,571

Issued: October 10, 2006

For: RESOURCE BOARD FOR EMULATION SYSTEM

REQUEST FOR CERTIFICATE OF CORRECTION

COMMISSIONER FOR PATENTS  
P.O. Box 1450  
Alexandria, Virginia 22313-1450

Attention: Patent Issue Division  
Decision and Certificate of Correction Branch

Sir:

It is requested that a Certificate of Correction be issued in accordance with the Form PTO-1050 that is enclosed herewith.

The column and line numbers where the errors occur in the issued patent are specified below. The source of the correction is indicated in parentheses following the correction.

Column 12

Line 48, "(RAN)" should be deleted and replaced with --(RAM)--. (RTOA mailed May 3, 2006, amended claim 2)

Line 57, "the" should be deleted and replaced with --The--. (ROTA mailed May 3, 2006, amended claim 3)

Line 62, "interface" should be deleted and replaced with --interface circuit--. (RTOA mailed May 3, 2006, amended claim 3)

Line 64, "response data" should be deleted and replaced with --response to the data--. (RTOA mailed May 3, 2006, amended claim 3)

Column 13

Line 5, "at least the" should be deleted and replaced with

--at least one--. (RTOA mailed May 3, 2006, amended claim 5)

Line 8, "and the" should be deleted and replaced with --and for--. (RTOA mailed May 3, 2006, amended claim 5)

Line 11, "terminals or" should be deleted and replaced with --terminals of--. (RTOA mailed May 3, 2006, amended claim 5)

Line 12, "PLOs" should be deleted and replaced with --PLDs--. (RTOA mailed May 3, 2006, amended claim 5)

Line 27, "PLD" should be deleted and replaced with --PLD,--. (RTOA mailed May 3, 2006, amended claim 8)

Line 28, "the clocking Logic" should be deleted and replaced with --for clocking logic--. (RTOA mailed May 3, 2006, amended claim 8)

Line 45, "at least output" should be deleted and replaced with --at least one output--. (Applicant error, amended claim 12)

Line 59, "separate ewe" should be deleted and replaced with --separate one--. (RTOA mailed May 3, 2006, amended claim 14)

Line 67, "separate of" should be deleted and replaced with --separate one of--. (Applicant error, claim 15)

#### Column 14

Line 23, "each and" should be deleted and replaced with --each FPGA and--. (RTOA mailed May 3, 2006, amended claim 17)

Line 29, "at least output" should be deleted and replaced with --at least one output--. (Applicant error, amended claim 17)

Line 37, "separate of the FPGA s" should be deleted and replaced with --separate one of the FGAs--. (Applicant error, amended claim 18)

Line 41, "FPGA s" should be deleted and replaced with --FGAs--. (Applicant error, amended claim 18)

Line 60, "wherein" should be deleted and replaced with --with--. (Applicant error, amended claim 21)

#### Column 15

Line 15, "pockets" should be deleted and replaced with --packets--. (RTOA mailed May 3, 2006, amended claim 23)

Line 21, "art" should be deleted and replaced with --an--. (RTOA mailed May 3, 2006, new claim 25)

Column 16

Line 6, "commmand" should be deleted and replaced with --command--. (RTOA mailed May 3, 2006, new claim 25)

Line 20, "an" should be deleted and replaced with --in--. (RTOA mailed May 3, 2006, new claim 25)

The applicant errors are obvious typographical or grammatical mistakes and add no new matter.

This patent is assigned of record to FORTELINK, INC., the assignment having been recorded on March 15, 2004 at Reel 014426, Frame 0906.

This request is made by the undersigned as attorney for the patentee/assignee under the Power of Attorney that was signed on December 10, 2003 and is of record in the Patent and Trademark Office.

Please send the certificate to the address indicated below.

Please charge \$100 for the fee under 37 CFR 1.20(a) to Deposit Account No. 19-2560.

Respectfully submitted,

/John Smith-Hill/  
John Smith-Hill  
Reg. No. 27,730

SMITH-HILL & BEDELL, P.C.  
16100 N.W. Cornell Road, #220  
Beaverton, Oregon 97006  
(503) 574-3100  
Docket: FORT 2769

UNITED STATES PATENT AND TRADEMARK OFFICE  
CERTIFICATE OF CORRECTION

PATENT NO. : 7,120,571  
DATED : October 10, 2006  
INVENTOR(S) : Sweyyan Shei, Ming Yang Wang, Vincent Chiu and Choo Ngui

It is certified that an error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Column 12

Line 48, "(RAN)" should be deleted and replaced with --(RAM)--.

Line 57, "the" should be deleted and replaced with --The--.

Line 62, "interface" should be deleted and replaced with --interface circuit--.

Line 64, "response data" should be deleted and replaced with --response to the data--.

Column 13

Line 5, "at least the" should be deleted and replaced with --at least one--.

Line 8, "and the" should be deleted and replaced with --and for--.

Line 11, "terminals or" should be deleted and replaced with --terminals of--.

Line 12, "PLOs" should be deleted and replaced with --PLDs--.

Line 27, "PLD" should be deleted and replaced with --PLD,--.

Line 28, "the clocking Logic" should be deleted and replaced with --for clocking logic--.

Line 45, "at least output" should be deleted and replaced with --at least one output--.

Line 59, "separate ewe" should be deleted and replaced with --separate one--.

Line 67, "separate of" should be deleted and replaced with --separate one of--.

Page 1 of 2

MAILING ADDRESS OF SENDER:

SMITH-HILL AND BEDELL, P.C.  
16100 N.W. Cornell Road, #220  
Beaverton, Oregon 97006

PATENT NO. 7,120,571  
ATTORNEY DOCKET FORT 2760

UNITED STATES PATENT AND TRADEMARK OFFICE  
CERTIFICATE OF CORRECTION

PATENT NO. : 7,120,571  
DATED : October 10, 2006  
INVENTOR(S) : Sweyyan Shei, Mint Yang Wang, Vincent Chiu and Choo Ngui

It is certified that an error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Column 14

Line 23, "each and" should be deleted and replaced with --each FPGA and--.

Line 29, "at least output" should be deleted and replaced with --at least one output--.

Line 37, "separate of the FPGA s" should be deleted and replaced with --separate one of the FPGAs--.

Line 41, "FPGA s" should be deleted and replaced with --FPGAs--.

Line 60, "wherein" should be deleted and replaced with --with--.

Column 15

Line 15, "pockets" should be deleted and replaced with --packets--.

Line 21, "art" should be deleted and replaced with --an--.

Column 16

Line 6, "commmand" should be deleted and replaced with --command--.

Line 20, "an" should be deleted and replaced with --in--.

2 of 2

MAILING ADDRESS OF SENDER:

SMITH-HILL AND BEDELL, P.C.  
16100 N.W. Cornell Road, #220  
Beaverton, Oregon 97006

PATENT NO. 7,120,571  
ATTORNEY DOCKET FORT 2769-